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Specification

BALUN

Technical Field

The present invention relates to a balun having three lines arranged in parallel with the ground surface.

Background

For the high-speed LSI for the recent wireless LAN, such as Bluetooth, the balanced-mode signals are in most cases produced in order to increase the signal noise margin.

As the wireless circuit generally employs an unbalanced circuit, however, the balun (balanced unbalanced conversion circuit) constitutes an essential element for performing this conversion.

The balun may comprise two branch circuits, that is, a $1/4$ -wavelength line and a $3/4$ -wavelength line.

When the balun includes the $1/4$ -wavelength line and $3/4$ -wavelength line, however, they have a different line length. Thus, even if a frequency can have a phase of 180 degrees with regard to the central frequency, the phase shift may be increased if the frequency is shifted within a band.

In view of the above fact, it is an object of the present invention to provide a balun in which the phase shift can be reduced significantly.

Summary of the Invention

In order to solve the problems described above, the inventors of the present invention have studied the problems extensively and earnestly, and have discovered that the problems can be solved by providing a balanced circuit that includes three lines each having the line length equal to $1/4$ of the wavelength at the central frequency in the working band. The present invention is thus based on the above discovery.

Specifically, the balun of the present invention is one that has three lines, that is, a first line, a second line and a third line, arranged in parallel with the ground surface, wherein the second line and the third line are arranged at the same height from the ground surface, the longitudinal length of each respective one of the first line, second line and third line is specified to a quarter of the central frequency in the working band, and the capacitance between the second line and the ground surface is specified to be equal to the capacitance between the second line and the first line.

According to the present invention, the distance between the center of each respective one of the second line and third line in the direction of the height and the ground surface located closer to the second line and third line is specified to be larger than the distance between the center of first line in the direction of the height and the center of each respective one of the second line and third line in the direction of the height. As one alternative, the permittivity of a dielectric between the plane formed by the center of each respective one of the second line and third line in the direction of the height may be specified to be less than the permittivity of a dielectric between the plane formed by the center of the first line in the direction of the height and the plane formed by the center of each respective one of the second line and third line in the direction of the height.

As a further alternative, the length of each respective one of the second line and third line in the direction of the width may be specified to be equal, the second line and third line may be arranged symmetrically with regard to the plane formed by the center of the first line in the direction of the width, one terminal of the first line being assumed as an input terminal for unbalanced-mode signals may be connected to one terminal of the third line as such input terminal, the other terminal of the first line and one terminal of the second line may be connected to the ground surface, respectively, and the other terminal of the second line and the other terminal of the third line may

be assumed as output terminals for balanced-mode signals, with the impedance of the input terminal for unbalanced-mode signals and the impedance of the output terminal for balanced-mode signals being specified to satisfy the following relationship defined below:

$$(C_a + C_{ac}) / \epsilon_0 = \epsilon_r^{1/2} \times Z_{air} / (Z_{in} \times Z_{out})^{1/2}$$

where C_a is the capacitance (C) between the second line and the ground surface, C_{ac} is the capacitance (C) between the second line and the third line, ϵ_0 is the permittivity in the vacuum, ϵ_r is the specific permittivity, Z_{air} is the characteristic impedance (Ω) in the vacuum, Z_{in} is the impedance (Ω) of the input terminal for unbalanced-mode signals, and Z_{out} is the impedance (Ω) of the output terminal for balanced-mode signals.

Fig. 1 is a sectional view illustrating one embodiment of the balun according to the present invention, and Fig. 2 is a top view illustrating one embodiment of the balun according to the present invention.

According to the present invention, the longitudinal length of each respective one of the first length a, second length a and third length c is specified to a quarter (1/4) of the wavelength of the central frequency in the working band, the second line a and third line c are arranged at the same height from the ground surface GC, and the capacitance C_a between the second line a and ground surface GC is set to be equal to the capacitance C_{ab} between the second line a and first line b (hereinafter, this equality will sometimes be referred to as " $C_a = C_{ab}$ ").

In the balun of the present invention, it is preferred that the distance between the center of each respective one of the second line a and third line c in the direction of the height and the ground surface GC located closer to the second line a and third line c (which will sometimes be referred to as " h_3 ") should be specified to be longer than the distance between the first line b in the direction of the height and the distance of each respective one of the second line a and third line c in the direction of the height (which will

sometimes be referred to as “h2”). In the following description, it should be noted that the distance between the center of the first line b in the direction of the height and the ground surface GC located closer to the first line b will sometimes be referred to as “h1”.

In the balun of the present invention, furthermore, it is preferred that instead of being $h2 < h3$, the permittivity ϵ_3 of a dialectic (which will sometimes be referred to as “D3”) between the plane formed by the center of each respective one of the second line a and third line c in the direction of the height and the ground surface GC located closer to the second line a and third line c should be specified to be less than the permittivity ϵ_2 of a dialectic (which will sometimes be referred to as “D2”) between the plane formed by the center of the first line b in the direction of the height and the plane formed by the center of each respective one of the second line a and third line c in the direction of the height (which will sometimes be referred to as “ $\epsilon_3 < \epsilon_2$ ”). In the following description, it should be noted that the dialectic between the plane formed by the center of the first line b and the ground surface located closer to the first line b will sometimes be referred to as “D1”.

In the balun of the present invention in which $h2 < h3$ is given, the dielectric having the relative permittivity ϵ_r is included so that the permittivity ϵ_r is equal for all of the dielectric 1, dielectric 2 and dielectric 3.

The following describes the results of the electromagnetic field analysis that was performed using the device simulator for the capacitance C_a between the second line a and ground surface GC and the capacitance C_b between the second line a and first line b.

Initially, for $h2 = h3 = 2$ micrometers being given, the capacitance C_a and the capacitance C_b have been examined to determine how those capacitances will change as the interval S_{ac} between the second line a and third line c is varied. The results are given in Fig. 3.

It may be seen from Fig. 3 that $C_a > C_b$ is satisfied for all of the

intervals S_{ac} that have been varied, but $C_a = C_{ab}$ is not satisfied.

Accordingly, it may be appreciated that at least $C_a = C_{ab}$ can be satisfied if any of the following conditions is satisfied.

a) $h_a < h_3$

If this condition is satisfied, the incremental amount of the capacitance C_a will become greater than that of the capacitance C_{ab} if the interval S_{ac} is increased. If $C_a < C_{ab}$ is true when the interval S_{ac} is initially small, enlarging the interval S_{ac} gradually will cause C_a versus C_{ab} to change like $C_a < C_{ab}$, $C_a = C_{ab}$, and $C_a > C_{ab}$. Thus, the interval S_{ac} that satisfies $C_a = C_{ab}$ can be estimated.

Fig. 4 shows how the capacitance C_a versus the capacitance C_{ab} will change if the interval S_{ac} is changed when $h_2 = 1.5$ micrometers and $h_3 = 2$ micrometers are given. It may be seen from Fig. 4 that $C_a = C_{ab}$ can be satisfied when the interval S_{ac} is about 10.3 micrometers.

b) $\epsilon_3 < \epsilon_2$

If this condition is satisfied, the interval S_{ac} that satisfies $C_a = C_{ab}$ is available even if $h_2 = h_3$.

If $\epsilon_3 < \epsilon_2$, the medium will become isotropic no longer in its strict sense of the word although it has little effect. The balun can thus be constructed as is the case with $h_2 < h_3$.

In the present invention, furthermore, it is preferred that the length of the second line a in the direction of the width (which will sometimes be referred to as " W_a ") should be specified to be equal to the length of the third line c in the direction of the width and shorter than the length of the first line b in the direction of the width (which will sometimes be referred to as " W_b "). It is also preferred that the thickness of each respective one of the first line b, second line a and third line c (which will sometimes be referred to as " t ") should be equally the same.

It is preferred that the second line a and third line c should be

arranged symmetrically with regard to the line formed by the center of the first line b in the direction of the width and its extension. Preferably, one terminal of the first line b should be assumed as the input terminal for unbalanced-mode signals which is connected to one terminal of the third line c, the other terminal of the first line b and one terminal of the second line a should be connected to the ground surface GC, respectively, and the other terminal of the second line a and the other terminal of the third line c should be assumed as the output terminal for balanced-mode signals.

It is preferred that the length of each respective one of the second line a and third line c in the direction of the width (W_a) and the interval between the second line a and third line c in the direction of the width (which will sometimes be referred to as "Sac") should be chosen as appropriate in order to satisfy the following relationship as well as the condition specified by $C_a = C_{ab}$.

$$(C_a + C_{ac}) / \epsilon_0 = \epsilon_r^{1/2} \times Z_{air} / (Z_{in} \times Z_{out})^{1/2}$$

where C_a is the capacitance (C) between the second line a and the ground surface GC, C_{ac} is the capacitance (C) between the second line a and the third line c, ϵ_0 is the permittivity in the vacuum, ϵ_r is the relative permittivity, Z_{air} is the characteristic impedance (Ω) in the vacuum, Z_{in} is the impedance (Ω) of the input terminal for unbalanced-mode signals, and Z_{out} is the impedance (Ω) of the output terminal for balanced-mode signals.

The following explains the process of deriving the above relationship, in which it is assumed that there is no loss since the line conductor loss and the dielectric loss are usually negligible.

The Y matrix (6 rows x 6 columns) for the three-line balun shown in Fig. 2 in which the line length is equal to a quarter of the wavelength at the central frequency in the working band may be given as follows:

$$Y = 1/k_u \begin{bmatrix} \omega C, & -\omega C(1-u^2)^{1/2} \\ -\omega C(1-u^2)^{1/2}, & \omega C \end{bmatrix}$$

where, ω refers to the frequency and $u = j \times \tan(KL)$ (K is the phase constant in the dielectric and L is the line length).

C is the C matrix for the 3 lines, that is,

$$C = \begin{bmatrix} C_a + C_{ab} + C_{ac}, & -C_{ab}, & -C_{ac} \\ -C_{ab}, & C_b + 2C_{ab}, & -C_{ab} \\ -C_{ac}, & -C_{ab}, & C_a + C_{ab} + C_{ac} \end{bmatrix}$$

where, C_b refers to the capacitance (C) between the first line and ground surface.

As the line length L is specified to be equal to a quarter of the wavelength at the central frequency in the working band, $1/u$ can approximate to zero (0), and $(1 - u^2)^{1/2} / u$ can approximate to $-j$.

Accordingly, the Y matrix can be arranged as follows:

$$Y = j\omega/k \begin{bmatrix} 0 & C \\ C & 0 \end{bmatrix}$$

$$Y = j\omega/k_z \begin{bmatrix} 0, & 0, & 0, & C_a + C_{ab} + C_{ac}, & -C_{ab}, & -C_{ac} \\ 0, & 0, & 0, & -C_{ab}, & C_b + 2C_{ab}, & -C_{ab} \\ 0, & 0, & 0, & -C_{ac}, & -C_{ab}, & C_a + C_{ab} + C_{ac} \\ C_a + C_{ab} + C_{ac}, & -C_{ab}, & C_{ac}, & 0, & 0, & 0 \\ -C_{ab}, & C_b + 2C_{ab}, & -C_{ab}, & 0, & 0, & 0 \\ -C_{ac}, & -C_{ab}, & C_a + C_{ab} + C_{ac}, & 0, & 0, & 0 \end{bmatrix}$$

In Fig. 2 and in the following description, it is assumed that the six terminals are designated as u terminal, v terminal, w terminal, x terminal, y

terminal and z terminal, respectively, and the input terminal 1 ($P_{in} 1$) is electrically connected to the v terminal of the first line and to the w terminal of the third line, and the y terminal of the first line and the u terminal of the second line are connected to the ground surface, respectively, with the x terminal of the second line being electrically connected to the output terminal ($P_{out} 2$) and the z terminal of the third line being electrically connected to the output terminal 3 ($P_{out} 3$).

Under the above assumption, the following equation will hold true:

$$V_u \text{ (voltage at u terminal)} = V_y \text{ (voltage at y terminal)} = 0$$

$$V_v \text{ (voltage at v terminal)} = V_w \text{ (voltage at w terminal)} = V_1 \text{ (voltage at input terminal 1)}$$

$$J_1 \text{ (current through input terminal 1)} = J_v \text{ (current through v terminal)} + J_w \text{ (current through w terminal)}$$

$$V_x \text{ (voltage at x terminal)} = V_2 \text{ (voltage at output terminal 2)}$$

$$J_x \text{ (current through x terminal)} = V_2 \text{ (voltage at output terminal 2)}$$

$$V_z \text{ (voltage at z terminal)} = V_3 \text{ (voltage at output terminal 3)}$$

$$J_z \text{ (current through z terminal)} = J_3 \text{ (current through output terminal 3)}$$

Accordingly, the following equation will hold true:

$$\begin{pmatrix} J_u \\ J_v \\ J_w \\ J_2 \\ J_y \\ J_3 \end{pmatrix} = j\omega / k_z \begin{pmatrix} 0 & 0 & 0 & C_a + C_{ab} + C_{ac} & -C_{ab} & -C_{ac} \\ 0 & 0 & 0 & -C_{ab} & C_b + 2C_{ab} & -C_{ab} \\ 0 & 0 & 0 & -C_{ac} & -C_{ab} & C_a + C_{ab} + C_{ac} \\ C_a + C_{ab} + C_{ac} & -C_{ab} & -C_{ac} & 0 & 0 & 0 \\ -C_{ab} & C_b + 2C_{ab} & -C_{ab} & 0 & 0 & 0 \\ -C_{ac} & -C_{ab} & C_a + C_{ab} + C_{ac} & 0 & 0 & 0 \end{pmatrix} \begin{pmatrix} 0 \\ V_1 \\ V_1 \\ V_2 \\ 0 \\ V_3 \end{pmatrix}$$

$$\begin{pmatrix} J_u \\ J_v \\ J_w \\ J_2 \\ J_y \\ J_3 \end{pmatrix} = j\omega / k_z \begin{pmatrix} (C_a + C_{ab} + C_{ac})V_2 - C_{ac}V_3 \\ -C_{ab}V_2 - C_{ab}V_3 \\ -C_{ac}V_2 + (C_a + C_{ab} + C_{ac})V_3 \\ -(C_{ab} + C_{ac})V_1 \\ (C_b + C_{ab})V_1 \\ (C_a + C_{ac})V_1 \end{pmatrix}$$

J_1 (current through input terminal 1) has the following relationship:

$$J_1 = J_v + J_w$$

$$J_1 = j\omega \{-C_{ab} \times V_2 - C_{ab} \times V_3 - C_{ac} \times V_2 + (C_a + C_{ab} + C_{ac})V_3\} / k_z$$

$$J_1 = j\omega \{-(C_{ab} + C_{ac}) \times V_2 + (C_a + C_{ac})V_3\} / k_z$$

Accordingly, the Y matrix for the three terminals may be given in terms of the following equation:

$$Y = j\omega / k_z \begin{pmatrix} 0, & -(C_{ab} + C_{ac}), & C_a + C_{ac} \\ -(C_{ab} + C_{ac}), & 0, & 0 \\ C_a + C_{ac}, & 0, & 0 \end{pmatrix}$$

Now, consider the case where the above Y matrix satisfies the conditions specified by the balun. Then, the lossless balun can satisfy the following condition, considering the central symmetry:

$$S_{11} = 0$$

$$S_{21} = -S_{31} = 2^{1/2} \times \exp(j\alpha) / 2$$

$$S_{22} - S_{32} = S_{33} - S_{23} = 0$$

As the balun is lossless, the following equation will hold true:

$$|S_{21}|^2 + |S_{22}|^2 + |S_{23}|^2 = 1$$

$$S_{ij} = S_{ji} \quad i, j = 1, 2, 3$$

Therefore, the S matrix may be given as follows:

$$S = \begin{pmatrix} 0, & 2^{-1/2} \exp(j\alpha), & -2^{-1/2} \exp(j\alpha) \\ 2^{-1/2} \exp(j\alpha), & S_{22}, & S_{22} \\ -2^{-1/2} \exp(j\alpha), & S_{22}, & S_{22} \end{pmatrix}$$

where, $|S_{22}|^2 = 1/4$, thus $S_{22} = \exp(j\beta) / 2$.

$$S = \begin{pmatrix} 0, & 2^{-1/2} \exp(j\alpha), & -2^{-1/2} \exp(j\alpha) \\ 2^{-1/2} \exp(j\alpha), & 1/2 \exp(j\beta), & 1/2 \exp(j\beta) \\ -2^{-1/2} \exp(j\alpha), & 1/2 \exp(j\beta), & 1/2 \exp(j\beta) \end{pmatrix}$$

The above equation assumes that all of the input and output terminals have the reference impedance, and if the above S matrix is converted into the Y matrix with the input/output terminal impedance being Z_{in} , $Z_{out} / 2$, the resulting Y matrix Yb will be obtained as follows:

$$Yb = \begin{pmatrix} Yb_{11}, & Yb_{12}, & Yb_{12} \\ Yb_{12}, & Yb_{22}, & Yb_{23} \\ Yb_{12}, & Yb_{23}, & Yb_{22} \end{pmatrix}$$

where,

$$Yb_{11} = -(1 + \exp(2j\alpha)) / \{Z_{in}(-1 + \exp(2j\alpha))\}$$

$$Yb_{12} = 2 \exp(j\alpha) / \{(Z_{in} \times Z_{out})^{1/2}(-1 + \exp(2j\alpha))\}$$

$$Yb_{22} = -2(1 + \exp(j(2\alpha + \beta))) / \{Z_{out}(-1 + \exp(2j\alpha))(1 + \exp(j\beta))\}$$

$$Yb_{23} = 2(\exp(j2\alpha) + \exp(j\beta)) / \{Z_{out}(-1 + \exp(2j\alpha))(1 + \exp(j\beta))\}$$

The Y matrix for the three terminals may be expressed in terms of the following equation:

$$Y = j\omega / k_2 \begin{pmatrix} 0, & -(C_{ab} + C_{ac}), & C_a + C_{ac} \\ -(C_{ab} + C_{ac}), & 0, & 0 \\ C_a + C_{ac}, & 0, & 0 \end{pmatrix}$$

In order that the above equation will be equal to the Y matrix of the input/output terminal impedance Z_{in} , $Z_{out}/2$, it is required that $\exp(2\alpha j) = -1$, $\exp(\beta j) = 1$ exist. For $\alpha = \pi/2$, $\beta = \pi$, Yb may be expressed as follows:

$$Y_b = \begin{bmatrix} 0, & -j/(Z_{in} \times Z_{out})^{1/2}, & j/(Z_{in} \times Z_{out})^{1/2} \\ -j/(Z_{in} \times Z_{out})^{1/2}, & 0, & 0 \\ j/(Z_{in} \times Z_{out})^{1/2}, & 0, & 0 \end{bmatrix}$$

The balun can be obtained if the construction of the C matrix is designed so that it can satisfy the following equations by comparing Y and Yb:

$$\begin{aligned} \omega (C_{ab} + C_{ac}) &= k_z / (Z_{in} \times Z_{out})^{1/2} \\ \omega (C_a + C_{ac}) &= k_z / (Z_{in} \times Z_{out})^{1/2} \end{aligned}$$

That is,

$$\omega (C_{ab} + C_{ac}) = \omega (C_a + C_{ac}) = k_z / (Z_{in} \times Z_{out})^{1/2}$$

Accordingly, the required condition can be expressed by the two equations given below:

$$\begin{aligned} C_a &= C_{ab} \\ v_p (C_a + C_{ac}) &= 1 / (Z_{in} \times Z_{out})^{1/2} \end{aligned}$$

As the relative permeability is approximately 1 for the ordinary metals, the phase velocity V_p may be expressed as follows, using $Z_{air} = (\mu_0 / \epsilon_0)^{1/2} = 120 \pi$.

$$\begin{aligned} v_p &= 1 / (\epsilon \mu)^{1/2} \\ v_p &= 1 / (\epsilon_r \epsilon_0 \mu_0)^{1/2} \\ v_p &= 1 / (\epsilon_0 \times Z_{air} \times \epsilon_r)^{1/2} \end{aligned}$$

Accordingly,

$$(C_a + C_{ac}) \approx 1 / v_p (Z_{in} \times Z_{out})^{1/2}$$

$$(C_a + C_{ac}) = \epsilon_0 \times Z_{air} \times \epsilon_r^{1/2} / (Z_{in} \times Z_{out})^{1/2}$$

From the above,

$$(C_a + C_{ac}) / \epsilon_0 = \epsilon_r^{1/2} \times Z_{air} / (Z_{in} \times Z_{out})^{1/2}$$

According to the present invention, the balun that provides any desired input/output impedance can be constructed by choosing the appropriate length of each respective one of the second line and third line in the direction of the width and the appropriate interval S_{ac} between the second line a and third line c, even if the particular input/output impedance is specified for the balun.

Fig. 5 presents the values of the interval S_{ac} , capacitance $(C_a + C_{ab})$, and input/output impedance $(Z_{in} \times Z_{out})^{1/2}$ that will satisfy the $C_a = C_{ab}$ condition by varying the length W_a of each respective one of the second line a and third line c in the direction of the width. It should be noted that the length W_b of the first line b in the direction of the width is fixed to 16 micrometers.

It may become apparent from Fig. 5 that the balun that provides any desired input/output impedance can be constructed by choosing the appropriate length W_a in the direction of the width and the appropriate interval S_{ac} , even if the particular input/output impedance is specified for the balun.

In the strip line-type balun according to the present invention, it should be noted that the property will remain unchanged if the absolute dimensions are varied because the aspect ratio can be the same. Thus, the same property can be obtained even if the aspect ratio is increased or decreased.

Brief Description of the Drawings

Fig. 1 is a sectional view illustrating one example of the balun according to the present invention, in which \underline{a} refers to a second line, \underline{b} refers to a first line, \underline{c} refers to a third line, GC refers to a ground surface, W_a refers to the length of each respective one of the second line \underline{a} and third line \underline{c} in the direction of the width, W_b refers to the length of the first line \underline{b} in the direction of the width, t refers to the thickness of the first line \underline{b} , second line \underline{a} and third line \underline{c} in the direction of the height, S_{ac} refers to the interval between the second line \underline{a} and third line \underline{c} in the direction of the width, and S_{ab} refers to the location of one end of the second line \underline{a} in the direction of the width when one end of the first line \underline{b} is given as a reference; it should be noted that when S_{ab} is positive, the one end of second line is situated outside the one end of the first line \underline{b} , and when S_{ab} is negative, the one end of the second line is situated inside the one end of the first line; the second line \underline{a} and third line \underline{c} are located symmetrically with regard to the line AA'; generally, h means the distance with regard to the ground surface GC; specifically, h_1 refers to the distance between the center of the first line \underline{b} in the direction of the height and the ground surface GC located closer to the first line \underline{b} , h_2 refers to the distance between the center of the first line \underline{b} in the direction of the height and the center of each respective one of the second line \underline{a} and third line \underline{c} in the direction of the height, and h_3 refers to the distance between the center of each respective one of the second line \underline{a} and third line \underline{c} in the direction of the height and the ground surface GC closer to the second line \underline{a} and third line \underline{c} ; D1 refers to a dielectric 1, D2 refers to a dielectric 2 and D3 refers to a dielectric 3;

Fig. 2 is a top view illustrating one example of the equivalent circuit diagram for the balun according to the present invention, in which P_{in1} refers to an input terminal 1, P_{out2} refers to an output terminal 2, and P_{out3} refers to an output terminal 3, with u , v , w , x , y , and z representing six terminals; it should be noted that the longitudinal length of each respective one of the first

line b, second line a and third line c is specified to be equal to one quarter of the wavelength at the central frequency in the working band;

Fig. 3 is a graph diagram that shows how the capacitance will be changed as the distance between the second line and third line is varied when $h_2 = h_3$;

Fig. 4 is a graph diagram that shows how the capacitance will be changed as the distance between the second line and third line is varied when $h_2 < h_3$;

Fig. 5 is a graph diagram that shows how the values for the distance S_{ab} , capacitance $(C_a + C_{ac})$, and input/output impedance $(Z_{in} \times Z_{out})^{1/2}$ will be changed to satisfy the condition of $C_a = C_{ab}$ as the length of each respective one of the second line and third line in the direction of the width is varied;

Fig. 6 is a graph diagram that shows the transmission characteristic to the output terminal 2 (S_{21}) and the transmission characteristic to the output terminal 3 (S_{31});

Fig. 7 is a graph diagram that shows the phase difference between the output terminal 2 and output terminal 3 when a signal is provided through the input terminal 1; and

Fig. 8 is a graph diagram that shows the variation of the reflection loss for the input terminal 1 (S_{11}) as well as the variation of the reflection that occurs when differential amplitude is provided to the output terminal 2 and output terminal 3.

Best Modes of Embodying the Invention

Several preferred embodiments of the present invention are now described below. It should be understood, however, that the present invention is not restricted to those embodiments, which may be modified in numerous ways without departing from the spirit and scope of the invention as defined in the appended claims.

(Embodiment 1)

In the first embodiment being described here, the balun of the present invention employs the construction in which it has the band of 2.45GHz, the input impedance of $50\ \Omega$ and the output impedance of $100\ \Omega$. Then, the balun was examined to check the performance.

Since $(Z_{in} \times Z_{out})^{1/2} = 70.7\ \Omega$ is given, $h2 = 1.5$ micrometers, $h3 = 2$ micrometers, $Wa = 3.35$ micrometers, $Sab = 0.17$ micrometers, $Sac = 8.96$ micrometers, and $Wb = 16$ micrometers are provided.

The line has the length of about 15.5 mm that generally corresponds to $1/4$ wavelength for the band of 2.45 GHz and permittivity of 3.6.

It should be noted that as the line has the length of 100 micrometers for the actual print circuit board, the above parameters may be multiplied by a factor of 100 such that $h2 = 150$ micrometers, $h3 = 200$ micrometers, $Wa = 335$ micrometers, $Sab = 17$ micrometers, $Sac = 896$ micrometers, and $Wb = 1600$ micrometers.

The results that have been obtained by the electromagnetic field simulation are shown in Figs. 6 through 8.

Fig. 6 is a graph diagram that shows the transmission characteristic for the balanced terminal or output terminal 2 (S_{21}) and the transmission characteristic for the balanced terminal or output terminal 3 (S_{31}) when a signal is provided through the unbalanced terminal or input terminal 1. It may be seen from Fig. 6 that the amplitude is almost equal for the band of 2.45 GHz.

Fig. 7 is a graph diagram that shows the phase difference between the output terminal 2 and output terminal 3 when a signal is provided through the input terminal 1. It may be seen from Fig. 7 that the phase that can occur is of substantially 180 degrees and the phase shift can be reduced significantly.

Fig. 8 shows the reflection factor for the input terminal 1 (S_{11}) as well as the reflection that occurs when differential amplitude is provided at the

output terminal 2 and output terminal 3 $((S_{22} + S_{23} - 2 \times S_{23}) / 2^{3/2})$, where S_{22} represents the reflection factor for the output terminal 2 and S_{23} represents the transmission factor from the output terminal 3 to the output terminal 2). It may be seen from Fig. 8 that less than 25dB is provided at 2.45 GHz and the desired output impedance can be obtained.

Possible Industrial Utilities

It may be appreciated from the foregoing description that the balun according to the present invention provides the performance that is equivalent to the commercially available chip components, and permits the phase shift to be reduced significantly. Thus, the balun can be incorporated in the multi-layer circuit board, and can flexibly meet the requirements for producing many kinds of components as well as the short-term production requirements. The balun can also meet the requirements for producing the drastically reduced size components.